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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Addison Chen

Application No.: 10/783,345

Filed: 2/20/2004

For: A SYSTEM AND METHOD FOR
GENERATING 128-BIT CYCLIC REDUNDANCY)
CHECK VALUES WITH 32-BIT GRANULARITY)

)
) Group Art Unit: Unassigned

)
) Examiner: Unassigned

)
) Atty. Docket No.:
) NVIDP341/P001326

)
) Date: December 23, 2004

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450 on December 23, 2004.

Signed:

Erica L. Farlow

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §§ 1.56 AND 1.97(b)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

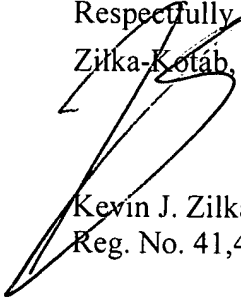
Dear Sir:

The references listed in the attached PTO Form 1449, copies of which are attached, may be material to examination of the above-identified patent application. Applicants submit these references in compliance with their duty of disclosure pursuant to 37 CFR §§ 1.56 and 1.97. The Examiner is requested to make these references of official record in this application.

This Information Disclosure Statement is not to be construed as a representation that a search has been made, that additional information material to the examination of this application does not exist, or that these references indeed constitute prior art.

This Information Disclosure Statement is believed to be filed before the mailing date of a first Office Action on the merits. Accordingly, it is believed that no fees are due in connection with the filing of this Information Disclosure Statement. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 50-1351 (**Order No. NVIDP341/P001326**).

Respectfully submitted,
Zilka-Kotab, PC



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| Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary) | Atty. Docket No. | Application No.: |
| | NVIDP341 | 10/783,345 |
| | Applicant: | |
| | Addison Chen | |
| | Filing Date: | Group Art Unit: |
| | 02/20/2004 | Unassigned |

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| | V | Chesson, Greg, "The Protocol Engine Project" Technology Focus September 1987 |
| Examiner | | Date Considered |

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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| Examiner Initial | No. | Document No. | Publication Date | Country or Patent Office | Class | Sub-class | Translation | |
|------------------|-----|--------------|------------------|--------------------------|-------|-----------|-------------|----|
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| | NVIDP341 | 10/783,345 |
| | Applicant: | |
| | Addison Chen | |
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| | A | | | | | | |

Foreign Patent or Published Foreign Patent Application

| Examiner Initial | No. | Document No. | Publication Date | Country or Patent Office | Class | Sub-class | Translation | |
|------------------|-----|--------------|------------------|--------------------------|-------|-----------|-------------|----|
| | | | | | | | Yes | No |
| | B | | | | | | | |

Other Documents

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| Form 1449 (Modified) Information Disclosure Statement By Applicant (Use Several Sheets if Necessary) | NVIDP341 Applicant: Addison Chen Filing Date: 02/20/2004 | Application No.: 10/783,345 Group Art Unit: Unassigned |
| | | |

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